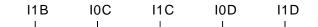
CMOS

IDTQS74FCT158AT/CT

2

DESCRIPTION:

The IDTQS74FCT158T is a high-speed CMOS TTL-compatible, quad, 2-input multiplexer. All inputs have clamp diodes for undershoot noise suppression. All outputs have ground bounce suppression. Outputs will not load an active bus when Vcc is removed from the device.



ABSOLUTE MAXIMUM RATINGS⁽¹⁾

Symbol	Description	Max	Unit
VTERM	Terminal Voltage with Respect to GND	–0.5 to +7	V
Tstg	Storage Temperature	-65 to +150	°C
Ιουτ	DC Output Current Max Current Sink/Pin	+120	mA
Ік	Input Diode Current, VIN < 0	-20	mA
Іок	DC Output Current, Vout < 0	-50	mA

NOTE:

 Stresses greater than those listed under ABSOLUTE MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

CAPACITANCE (TA = +25°C, F = 1.0MHz)

Symbol	Parameter ⁽¹⁾	Conditions	Тур.	Мах.	Unit
CIN	Input Capacitance	VIN = 0V	4	_	pF
Соит	Output Capacitance	Vout = 0V	8	_	pF

NOTE:

1. This parameter is measured at characterization but not tested.

PIN DESCRIPTION

/ER OPERATING RANGE

Conditions		Min.	Typ. ⁽¹⁾	Max.	Unit
el		2	—	—	V
		-	—	0.8	V
		-	0.2	_	V
	0 ≤ VIN ≤ VCC	-	—	±5	μA
)		-60	_	—	mA
= 25°C ⁽²⁾		_	-0.7	-1.2	V
	Iон = –15mA	2.4	_	_	V
	Iol = 48mA	—	—	0.5	V

OPERATING RANGE⁽¹⁾

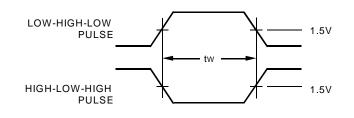
74FCT158AT		74FCT158CT		
	Max.	Min.	Max.	Unit
	5	1.5	4.3	ns
	7	1.5	5.2	ns
	6	1.5	4.8	ns

SWITCH POSITION

Test	Switch
Open Drain Disable Low Enable Low	Closed
All Other Tests	Open

DEFINITIONS:

 C_L = Load capacitance: includes jig and probe capacitance. RT = Termination resistance: should be equal to ZouT of the Pulse Generator.



- SO
- Small Outline IC (gull wing) Quarter Size Small Outline Package Q
- Small Outline IC (gull wing) S1
- High-Speed CMOS Quad 2-Input Multiplexer 158AT 158CT
- -40°C to +85°C 74