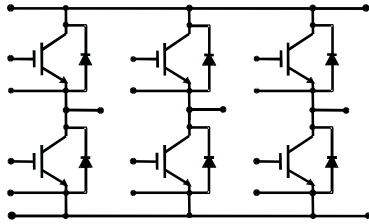


$V_{CE} = 1700\text{ V}$

$I_C = 150\text{ A}$



IGBT Module LoPak4 SPT

5SNS 0150V172100

MARKETING INFORMATION

Doc. No. 5SYA1536-00 Sep. 01

- Low-loss, rugged IGBT SPT chip-set
- EMC friendly diode with positive temp. coefficient of on-state
- Low profile compact baseless package
- Snap-on PCB assembly
- Integrated PTC substrate temperature sensor
- Low thermal resistance version



Maximum Rated Values

($T_{vj} = 25^\circ\text{C}$, unless specified otherwise)

Parameter	Symbol	Conditions	Values	Unit
Collector-Emitter Voltage	V_{CES}	V_{GE} shorted	1700	V
DC Collector Current	I_C	$T_{hs} = 85^\circ\text{C}$	150	A
Peak Collector Current	I_{CM}	Pulse: $t_p=1\text{ms}$, $T_{hs} = 85^\circ\text{C}$	300	A
Gate Emitter Voltage	V_{GES}		± 20	V
Total Power Dissipation	P_{tot}	$T_{hs} = 25^\circ\text{C}$ per switch	890	W
IGBT Switching SOA	SwSOA	$I_C = 300\text{ A}$, $V_{CEM} = 1700\text{ V}$, $V_{CC} \leq 1200\text{ V}$, $V_{GE} = \pm 15\text{ V}$, $T_{vj} = 125^\circ\text{C}$ voltages measured on auxiliary terminals		
IGBT Short Circuit SOA	SCSOA	$V_{CC} = 1200\text{ V}$, $V_{CEM} = 1700\text{ V}$, $t_p = 10\ \mu\text{s}$, $V_{GE} = \pm 15\text{ V}$, $T_{vj} = 125^\circ\text{C}$		
DC Forward Current	I_F		150	A
Peak Forward Current	I_{FM}	Pulse: $t_p = 1\text{ms}$, $T_{hs} = 85^\circ\text{C}$	300	A

Maximum Rated Values (cont.) ($T_{vj} = 25^{\circ}\text{C}$, unless specified otherwise)

Parameter	Symbol	Conditions	Values	Unit
Junction Temperature	T_{vj}		- 40 ~ 150	$^{\circ}\text{C}$
Storage Temperature	T_{tstg}/T_{cop}		- 40 ~ 125	$^{\circ}\text{C}$
Isolation Voltage	V_{iso}	1 min, $f = 50\text{Hz}$	4000	V
Mounting	Base to Heatsink	(M6) Hole 6.5mm diameter	2 ~ 3	Nm
	Main Terminals	M6 screws, max. insertion depth :10mm	3 ~ 5	Nm
	PCB mounting	Self tapping screw, Hole 2.5mm diameter, 6.0mm deep		
	Gate, Emitter Aux.	Spring pins, pitch of pins = 4mm, pcb thickness = 1.6mm		

IGBT Characteristic Values ($T_{vj} = 25^{\circ}\text{C}$, unless specified otherwise)

Parameter	Symbol	Conditions	min.	typ.	max.	Unit
Collector-Emitter Saturation Voltage	$V_{CE(sat)}$ *	$I_C = 150\text{ A}$, $V_{GE} = 15\text{ V}$	$T_{vj} = 25^{\circ}\text{C}$	2.40	2.80	V
			$T_{vj} = 125^{\circ}\text{C}$	2.80		V
Collector Cut-off Current	I_{CES}	$V_{CE} = 1700\text{ V}$, $V_{GE} = 0\text{ V}$, $T_{vj} = 125^{\circ}\text{C}$			TBD	mA
Gate-Emitter leakage Current	I_{GES}	$V_{CE} = 0\text{ V}$, $V_{GE} = \pm 20\text{ V}$, $T_{vj} = 125^{\circ}\text{C}$			± 500	nA
Gate-Emitter Threshold Voltage	$V_{GE(TO)}$	$I_C = 20\text{ mA}$, $V_{CE} = V_{GE}$	4.5		6.5	V
Total Gate Charge	Q_{ge}	$I_C = 150\text{ A}$, $V_{CE} = 900\text{ V}$, $V_{GE} = -15\text{ to }15\text{ V}$		TBD		nC
Input Capacitance	C_{ies}	$V_{CE} = 25\text{ V}$, $V_{GE} = 0\text{ V}$, $f = 1\text{MHz}$		TBD		nF
Output Capacitance	C_{oes}			TBD		nF
Reverse Transfer Capacitance	C_{res}			TBD		nF
Turn-On Delay Time	$t_{d(on)}$		$I_C = 150\text{ A}$, $V_{CC} = 900\text{ V}$, $R_{gon} = 10\ \Omega$, $T_{vj} = 125^{\circ}\text{C}$, $V_{GE} = \pm 15\text{ V}$		TBD	
Rise Time	t_r			TBD		μs
Turn-Off Delay Time	$t_{d(off)}$	$I_C = 150\text{ A}$, $V_{CC} = 900\text{ V}$, $R_{goff} = 10\ \Omega$, $T_{vj} = 125^{\circ}\text{C}$, $V_{GE} = \pm 15\text{ V}$		TBD		μs
Fall Time	t_f			TBD		μs
Turn-on Switching Energy	E_{on}	$R_{gon} = 10\ \Omega$, $I_C = 150\text{ A}$, $T_{vj} = 125^{\circ}\text{C}$, $V_{CC} = 900\text{ V}$, $V_{GE} = \pm 15\text{ V}$,		60.0		mJ
Turn-off Switching Energy	E_{off}	$R_{goff} = 10\ \Omega$, inductive load, integrated up to: 3% V_{CE} (E_{on}), 1% I_C (E_{off})		40.0		mJ
Module stray Inductance Plus to Minus	L_{sDC}				20	nH
Resistance terminal-chip	$R_{CC'+EE'}$		$T_{hs} = 25^{\circ}\text{C}$	1.40		m Ω
			$T_{hs} = 125^{\circ}\text{C}$	1.90		

* Note 1: Collector emitter saturation voltage is given at die level.

Diode Characteristic Values(T_{vj} = 25°C, unless specified otherwise)

Parameter	Symbol	Conditions	min.	typ.	max.	Unit
Forward Voltage	V _F *	I _F = 150 A	T _{vj} = 25 °C	1.90	2.30	V
			T _{vj} = 125 °C	2.00		
Reverse Recovery Current	I _{rrm}	I _F = 150 A, R _{gon} = 10 Ω, V _{CC} = 900 V, V _{GE} = ±15 V, T _{vj} = 125 °C		TBD		A
Reverse Recovery Charge	Q _{rr}			TBD		μC
Reverse Recovery Time	t _{rr}			TBD		μs
Reverse Recovery Energy	E _{rec}	I _F = 150 A, T _{vj} = 125 °C, V _{CC} = 900 V, R _{gon} = 10 Ω, V _{GE} = ±15 V, inductive load, fully integrated		24.0		mJ
Resistance terminal-chip	R _{CC'+EE'}		T _{hs} = 25 °C	1.40		mΩ
			T _{hs} = 125 °C	1.90		

* Note 2: Forward voltage is given at die level

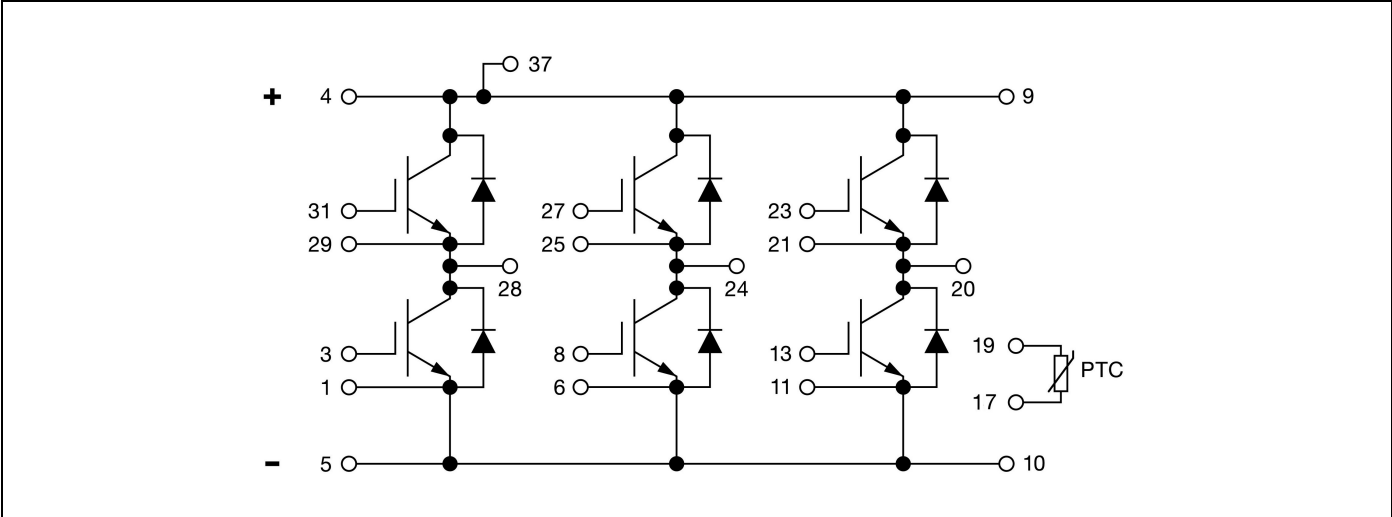
Thermal Characteristics(T_j = 25°C, unless specified otherwise)

Parameter	Symbol	Conditions	min.	typ.	max.	Unit
IGBT Thermal Resistance Junction to Heatsink	R _{th j-h} Igbt	Heatsink: flatness < +/- 50 μm, roughness < 6 μm without ridge Thermal grease: thickness: 30 μm < t < 50 μm			0.140	°C/W
Diode Thermal Resistance Junction to Heatsink	R _{th j-h} Diode				0.250	°C/W
Equivalent IGBT Thermal Resistance Junct. to Case	R _{th j-c} Igbt				0.065	°C/W
Equivalent Diode Thermal Resistance Junct. to Case	R _{th j-c} Diode				0.125	°C/W
Temperature sensor	PTC		Thermistor : R=1kΩ ±3%@25°C,B-value (25°C/100°C): -760K ±2%			

Mechanical Properties

Parameter	Symbol	Conditions	min.	typ.	max.	Unit
Dimensions	L*W*H	Typical , see outline drawing	184.5 * 106.5 * 34.5			mm
Clearance Distance	D _C	acc. IEC 664-1 and prEN50124-1:1995	Term. to base:	13.5		mm
			Term. to term:	8		mm
Surface Creepage Distance	D _{sc}	acc. IEC 664-1 and prEN50124-1:1995	Term. to base:	14		mm
			Term. to term:	11.5		mm
Weight				330		gr

Electrical configuration



Outline drawing

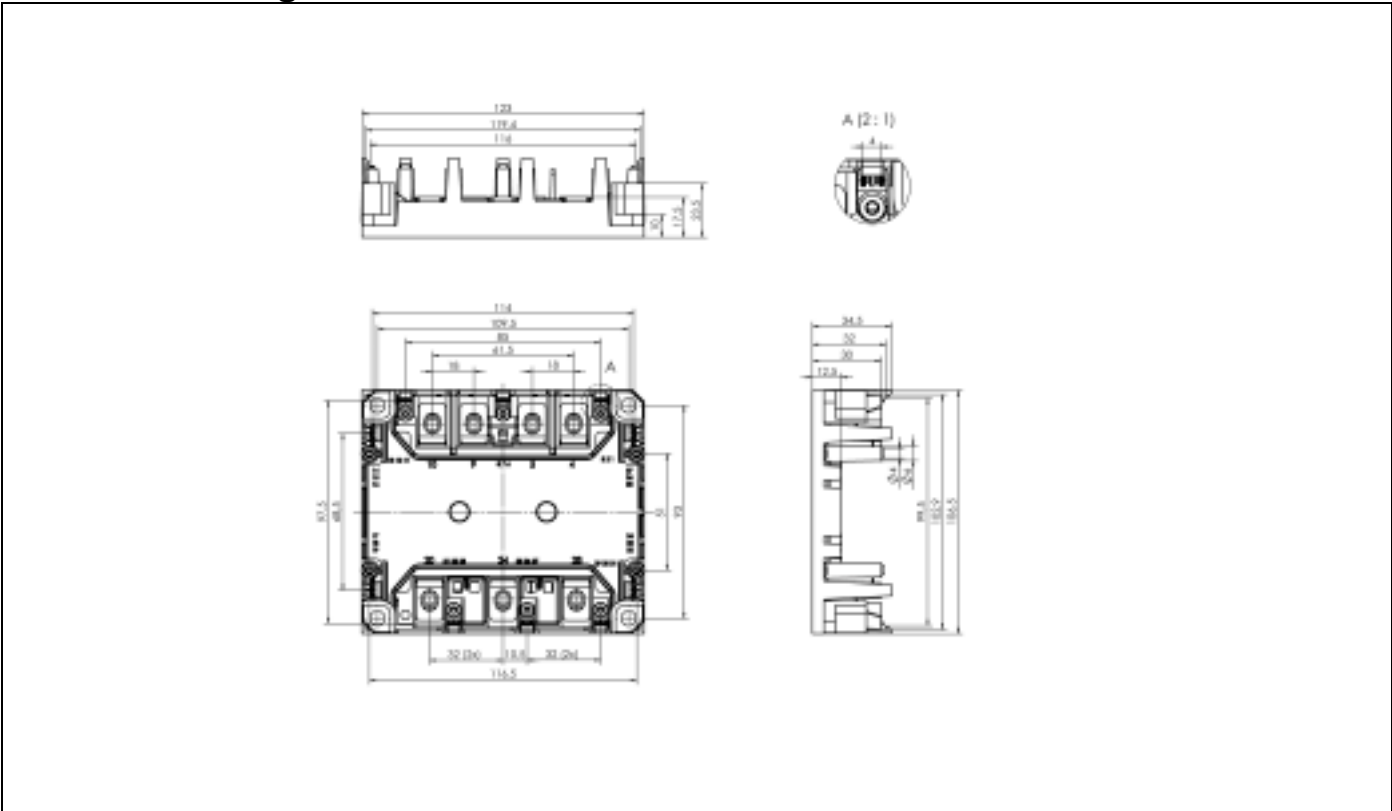


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