



## **REG5601**

# 18-LINE SCSI ACTIVE TERMINATOR

## **FEATURES**

- COMPLIES WITH SCSI-2 SPECIFICATIONS
- INTERNAL 2.9V REGULATOR
- ON-CHIP TERMINATION RESISTORS
- DISCONNECT ALL TERMINATION RESISTORS WITH A SINGLE LOGIC COMMAND
- POWER-DOWN MODE: 150µA max
- LINE CAPACITANCE: 10pF typ
- CURRENT LIMIT AND THERMAL SHUT-DOWN PROTECTION
- 28-Lead SOIC and SSOP PACKAGES

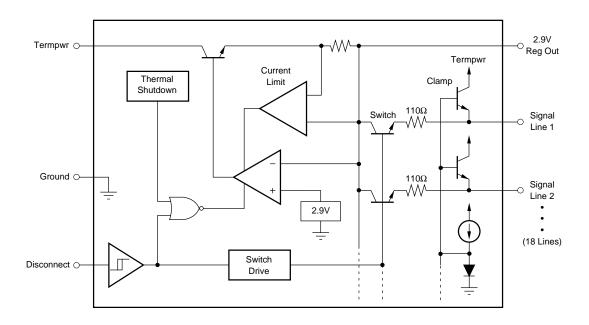
## DESCRIPTION

The REG5601 is an 18-line active terminator for SCSI-2 (Small Computer Systems Interface) circuitry. On-chip resistors and 2.9V regulator provide the prescribed  $110\Omega$  termination for low power dissipation and high speed data transmission.

All line connections can be disconnected from the bus with a single logic control line to reduce standby power consumption. Output lines remain high impedance without power applied. Each line is individually clamped at ground to dissipate negative-going glitches.

The 2.9V regulator is current-limited and thermally protected. Regulated output is available for external circuitry.

The REG5601 is available in 28-lead SOIC and finepitch SSOP packages and is specified for operation over the 0°C to 70°C temperature range.



International Airport Industrial Park • Mailing Address: PO Box 11400 • Tucson, AZ 85734 • Street Address: 6730 S. Tucson Blvd. • Tucson, AZ 85706 Tel: (520) 746-1111 • Twx: 910-952-1111 • Cable: BBRCORP • Telex: 066-6491 • FAX: (520) 889-1510 • Immediate Product Info: (800) 548-6132

## **SPECIFICATIONS**

 $T_A = 0$ °C to +70°C, Termpwr = 4.75V, and Disconnect = 0V unless otherwise specified.

		REG5601U			REG5601E			
PARAMETERS	CONDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	UNIT
POWER SUPPLY								
Termpwr Supply Voltage		4.0		5.25	4.0		5.25	V
Termpwr Supply Current	All Termination Lines = Open		14	25		14	25	mA
	All $V_{TERM} = 0.5V$		385	430		385	430	mA
Power-Down Mode	Disconnect = Open (High)		100	150		100	150	μΑ
TERMINATION LINES								
Termination Impedance	$\Delta I_{TERM} = 5mA$ to 15mA	107	110	115	107	110	115.5	Ω
Output High Voltage	Termpwr = $4V^{(1)}$	2.65	2.8		2.65	2.8		V
Maximum Output Current	$V_{TERM} = 0.5V$	20.5	21.7	22.4	19.8	21.7	22.4	mA
	Termpwr = 4V, $V_{TERM} = 0.5V^{(1)}$	19.4	21	22.4	19	21	22.4	mA
Output Clamp Level	$I_{TERM} = -30mA$	-0.2	-0.05	0.1	-0.2	-0.05	0.1	V
Output Leakage	Disconnect = Open (High), Tempwr = 0V to 5.25V		20	400		20	400	nA
Output Capacitance	Disconnect = Open (High)		10			10		pF
REGULATOR								
Regulator Output Voltage		2.8	2.9	3.0	2.8	2.9	3.0	V
Line Regulation	Termpwr = 4V to 6V		6	20		6	20	mV
Load Regulation	$I_{REG} = 0$ to $400mA$		20	50		20	50	mV
Drop-Out Voltage	All $V_{TERM} = 0.5V$ , $\Delta V_{REG} = 100mV$		1.0	1.2		1.0	1.2	V
Short-Circuit Current	$V_{REG} = 0V$	450	1350	1650	450	1350	1650	mA
Current Sink	$V_{REG} = 3.5V$	8	11		8	11		mA
Thermal Shutdown			170			170		°C
DISCONNECT LOGIC INPUT								
Disconnect Threshold		0.8	1.6	2.0	0.8	1.6	2.0	V
Threshold Hysterisis			200			200		mV
Input Current (Internal Pull-Up)	Disconnect = 0V		6	15		6	15	μΑ
TEMPERATURE RANGE								
Operating		0		70	0		70	°C
Storage		-40		150	-40		150	°C
$\theta_{ m JL}$ (junction to lead)			20			20		°C/W

NOTE: (1) Measurement of each termination line while the other 17 lines are held low (0.5V).

#### **CONNECTION DIAGRAM**

		1	SOIC SSOP		
1 0		28	GND		
2		27	Termination Line 18		
3		26	Termination Line 17		
4		25	Termination Line 16		
5		24	Termination Line 15		
6 7		23	Termination Line 14		
		22	GND (Thermal)		
8	REG5601	21	GND (Thermal)		
9		20	GND (Thermal)		
10		19	Termination Line 13		
11		18	Termination Line 12		
Termination Line 8 12		17	Termination Line 11		
13	13		Termination Line 10		
14		15	Reg Out		
NOTE: Pin 28 is electrical ground. Connect pins					
7, 8, 9, 20, 21, 22 to large circuit trace for proper heat					
	2 3 4 5 6 7 8 9 10 11 12 13	2 3 4 5 6 7 8 9 10 11 12 13 14 in 28 is electrical ground	2 27 3 26 4 25 5 24 6 23 7 REG5601 21 9 20 10 19 11 18 12 17 13 16 14 15		

sinking. Connect to ground or leave open-circuit.

## **ABSOLUTE MAXIMUM RATINGS**

Termpwr Voltage	+7V
Signal Line Voltage	0V to +7V
Regulator Output Current	1 65 \
Power Dissipation	2.5VV
Operating Junction Temperature	–40°C to +150°C
Storage Temperature	40°C to +150°C

## **PACKAGE/ORDERING INFORMATION**

MODEL	PACKAGE	PACKAGE DRAWING NUMBER <sup>(1)</sup>
REG5601U	Plastic 28-Lead SOIC	217
REG5601E	Plastic 28-Lead SSOP	324

NOTE: (1) For detailed drawing and dimension table, please see end of data sheet, or Appendix D of Burr-Brown IC Data Book.



This integrated circuit can be damaged by ESD. Burr-Brown recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

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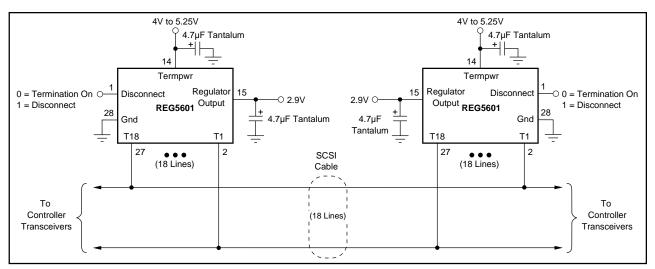


FIGURE 1. Standard SCSI Termination Application.

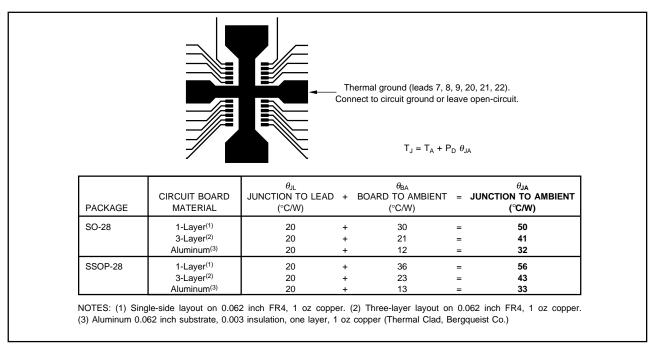


FIGURE 2. Circuit Board Layout.

protect it from damage during output short-circuit or overload. The current limit is approximately 1350mA and thermal shutdown activates at a junction temperature of approximately 175°C. For good reliability, the junction temperature should not exceed 125°C. Any tendency to activate the thermal shutdown during normal operation is an indication of inadequate heat sinking and/or excessive power dissipation. Heat is dissipated primarily by conduction through the leads to circuit board traces. It is important to connect the six thermal ground leads (7, 8, 9, 20, 21, 22) to a large circuit trace—see Figure 2. Measured values of thermal resistance for various circuit board materials are shown. These are

approximate values. Variations in circuit board pattern,

mounting techniques, air flow, proximity to other circuit boards and heat sources will affect thermal performance.

The REG5601 has current limit and thermal shutdown that

maximum recommended junction temperature of the REG5601 will not be exceeded. The procedure uses the internal thermal shutdown feature of the REG5601 (at  $T_J \approx 175^{\circ}\text{C}$ ) to determine when the junction is approximately 50°C above the maximum recommended junction temperature ( $T_J = 125^{\circ}\text{C}$ ). Operate the circuit with normal or other desired test electrical conditions. Increase the ambient temperature and determine the value at which thermal limit occurs (by sensing a sudden drop in  $V_{REG}$  output). At this point  $T_J$  is approximately 175°C. If this occurs at an ambient temperature of more than 50°C above the system ambient temperature design goal, the  $T_J$  will not exceed 125°C under the same electrical conditions when the ambient temperature is at the system design goal value.

A simple experiment will determine whether the actual circuit board layout is adequate (i.e.,  $\theta_{BA}$  is low enough) so that the



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