

# Digital transistors (built-in resistors)

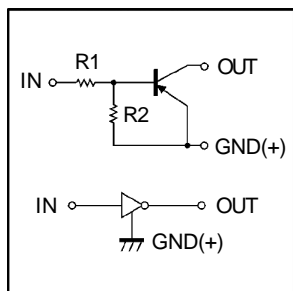
**• Features**

- 1) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 2) The bias resistors consist of thinfilm resistors with complete isolation to allow positive biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- 3) Only the on/ off conditions need to be set for operation, making device design easy.

**• Structure**

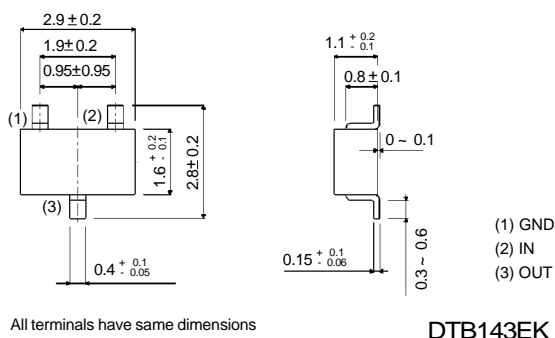
PNP digital transistor (with built-in resistors)

**•Equivalent circuit**

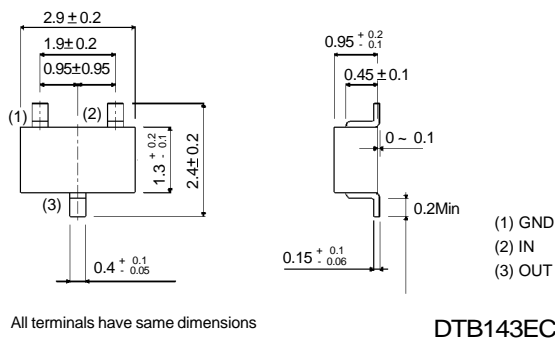


## DTB143EK

## DTB143EC



EIAJ: SC—59



EIAJ: SOT—23

**• Absolute maximum ratings(T<sub>a</sub>=25 °C)**

Parameter	symbol	limits	unit
Supply voltage	V <sub>cc</sub>	-50	V
Input voltage	V <sub>IN</sub>	-30~+10	V
Output current	I <sub>C</sub>	-500	mA
Power dissipation	P <sub>d</sub>	200   300	mW
Junction temperature	T <sub>j</sub>	150	°C
Storage temperature	T <sub>stg</sub>	-55~+150	°C

DTB143EK DTB143EC

● Electrical characteristics( $T_a=25^\circ\text{C}$ )

Parameter	symbol	Min.	Typ.	Max.	Unit	Conditions
Input voltage	$V_{I(off)}$	—	—	-0.5	V	$V_{CC} = -5\text{V}, I_O = -100\mu\text{A}$
	$V_{I(on)}$	-3	—	—		$V_O = -0.3\text{V}, I_O = -20\text{mA}$
Output Voltage	$V_{O(on)}$	—	—	-0.3	V	$I_O / I_I = -50\text{mA} / -2.5\text{mA}$
Input current	$I_I$	—	—	-1.8	mA	$V_I = -5\text{V}$
Output current	$I_{O(off)}$	—	—	-0.5	$\mu\text{A}$	$V_{CC} = -50\text{V}, V_I = 0\text{V}$
DC current gain	$G_I$	47	—	—	—	$V_O = -5\text{V}, I_O = -50\text{mA}$
Input resistance	$R_I$	3.29	4.7	6.11	$\text{k}\Omega$	—
Resistance ratio	$R_2 / R_1$	0.8	1	1.2	—	—
Transition frequency	$f_T$	—	200	—	MHz	$V_{CE} = -10\text{V}, I_E = 5\text{mA}, f = 100\text{MHz}^*$

\*Transition frequency of the device

ELECTRICAL CHARACTERISTIC CURVES

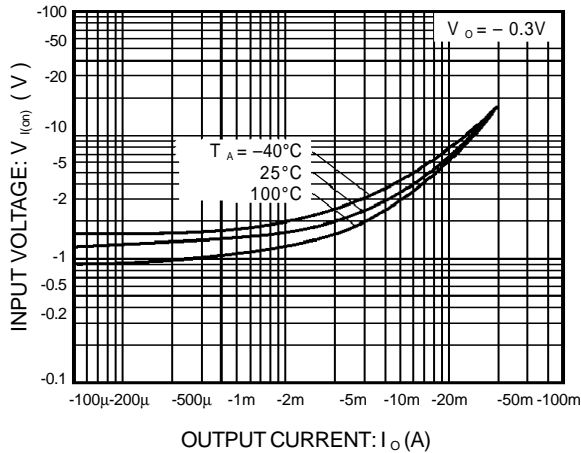


Figure 1. Input voltage vs. output current (ON characteristics)

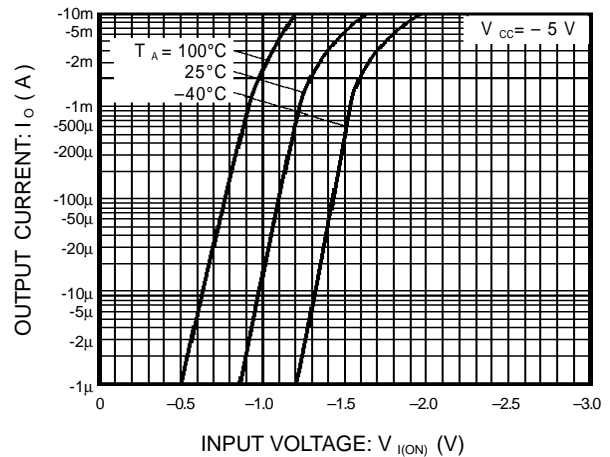


Figure 2. Output current vs. input voltage (OFF characteristics)

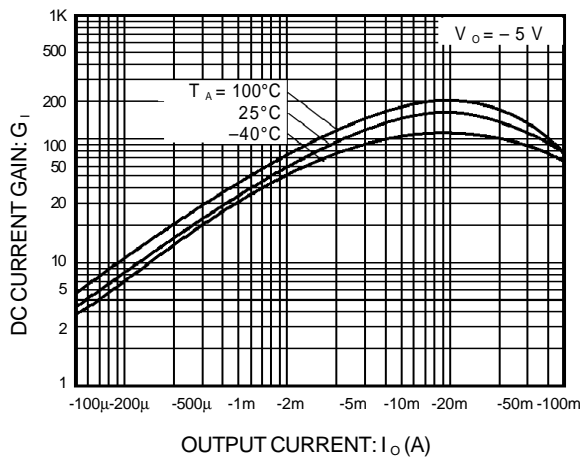


Figure 3. DC current gain vs. output current

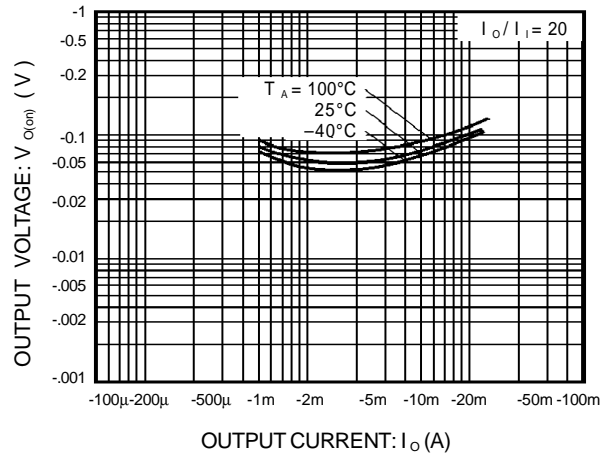


Figure 4. Output voltage vs. output current