

MOS INTEGRATED CIRCUIT μ PD16823

MONOLITHIC H BRIDGE DRIVER

DESCRIPTION

This IC is a monolithic H bridge driver employing a horizontal N-channel power MOS FET for its driver stage. It is provided with forward/reverse and brake functions and is ideal as a driver circuit for a motor that winds or rewinds the film in a camera, or a motor for moving a lens.

FEATURES

High drive current IDR1 = 0.5 A (DC)

 $I_{DR2} = 1$ A: at PW ≤ 200 ms, duty cycle $\leq 50\%$ $I_{DR3} = 3$ A: at PW ≤ 200 ms, single pulse

- 1.5ch H bridge circuits
- Low ON resistance (sum of ON resistance of top and bottom FETs)

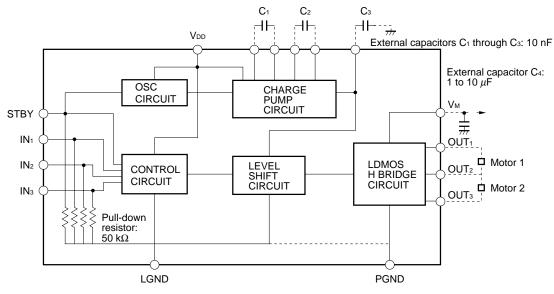
Ron =
$$0.6 \Omega$$
 TYP. at IDR = $0.5 A$

- Standby function that turns OFF charge pump circuit
- Low-voltage drive (2.5 V MIN.)
- Surface-mount mini-mold package: 20-pin plastic SOP (300 mil)

ORDERING INFORMATION

Part Number	Package
μPD16823GS	20-pin plastic SOP (300 mil)

BLOCK DIAGRAM



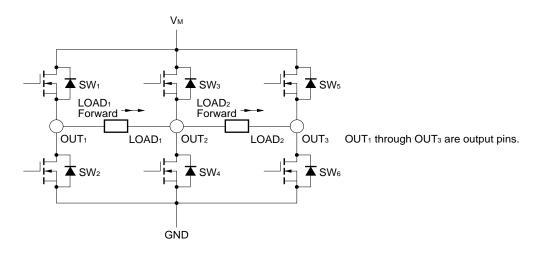
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PIN CONFIGURATION

1	C ₂ L	С2н	20
2	С1н	Vg	19
3	C ₁ L	STBY	18
4	Vм	OUT₃	17
5	OUT ₂	PGND	16
6	Vм	OUT ₁	15
7	V _{DD}	PGND	14
8	IN ₂	NC	13
9	IN ₃	DGND	12
10	IN ₁	NC	11

INTERNAL CONNECTION



FUNCTION TABLE

	Input	Signal		Circuit Operation Current Path		O De th	
IN ₁	IN ₂	INз	STBY			Circuit Operation Current P	
L	Н	L	Н	1 CH forward mode	SW ₁	\rightarrow LOAD ₁	→SW4
L	L	Н	Н	1 CH reverse mode	SW ₃	$\!\to\! \! LOAD_1$	\rightarrow SW ₂
L	Н	Н	Н	1 CH brake mode	SW ₂ (Di ₂)	\rightarrow LOAD1	→SW4
Н	Н	L	Н	2 CH forward mode	SW ₃	\rightarrow LOAD2	→SW ₆
Н	L	Н	Н	2 CH reverse mode	SW ₅	\rightarrow LOAD2	→SW4
Н	Н	Н	Н	2 CH brake mode	SW ₄ (Di ₄)	\rightarrow LOAD ₂	→SW ₆
×	L	L	Н	Stop mode			
×	×	×	L	Standby mode	Charge pur	mp ON/OFF	

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ABSOLUTE MAXIMUM RATINGS (TA = 25 $^{\circ}$ C)

Parameter	Symbol	Condition	Ratings	Unit
(Positive) supply voltage	V _{DD}	When charge pump operates	-0.5 to +6.5	V
		When charge pump does not operate	-0.5 to +8.0	V
	Vм		-0.5 to +8.0	V
Gate drive voltage	Vg		15	V
Input voltage	Vin		-0.5 to V _{DD} + 0.5	V
H bridge drive current	Idr	DC	±0.5	А
Positive: MOS output stage forward current		PW ≤ 200 ms, duty cycle ≤ 50%	±1.0	А
Negative: Output stage diode current		PW ≤ 200 ms, single pulse	±3.0	А
Power dissipation	Po		1.0	W
Operating temperature	TA		-30 to +60	°C
Junction temperature	T _{j(peak)}		150	°C
Storage temperature	T _{stg}		-55 to +150	°C

RECOMMENDED OPERATING CONDITIONS (TA = 25 ^{\circ}C)

Parameter	Symbol	Condition	Ratings	Unit
(Positive) supply voltage	V _{DD}	When charge pump operates	2.5 to 6.0	V
		When charge pump does not operate	2.5 to 7.5	V
	Vм		-0.5 to +7.5	V
Gate drive voltage	Vg		11 to 14	V
Junction temperature	T _{j(peak)}		125	°C

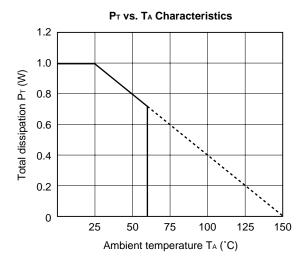
ELECTRICAL CHARACTERISTICS (TA = -30 °C to +60 °C)

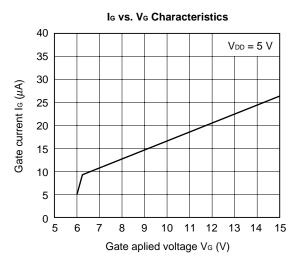
Parameter	Symbol	Condition	MIN.	TYP.	MAX.	Unit
V _{DD} pin current	I _{DD1}	V _{DD} = 5 V, with all control pins at high level			2.0	mA
	I _{DD2}	V _{DD} = 5 V, with all control pins at low level			10	μΑ
V _M pin current	Ім	T _A = 25 °C, with all control pins at low level			1.0	μΑ
		With all control pins at low level			10	μΑ
H bridge ON resistance	Ron	IDR = 0.5 A, VDD = VM = 5 V, TA = 25 °C		0.6	0.8	Ω
Control pin high-level input voltage	VIH		V _{DD} × 0.6			V
Control pin low-level input voltage	VIL				V _{DD} × 0.2	V
Charge pump circuit turn-off time	tonc	V _{DD} = V _M = 5 V			1.0	ms
H bridge circuit turn-ON time	tonh	$C_1 = C_2 = C_2 = 10 \text{ nF}$			10	μs
H bridge circuit turn-OFF time	toffh	IDR = 0.5 A			5.0	μs
Regenerative diode voltage drop	VF	IF = 0.5 A		1.0		V
Control pin input pull-down resistance	Rin		25	50	75	kΩ

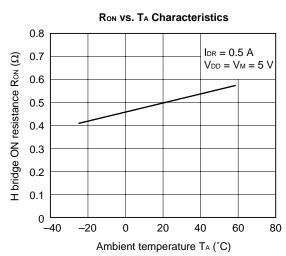
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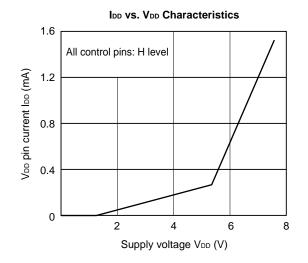


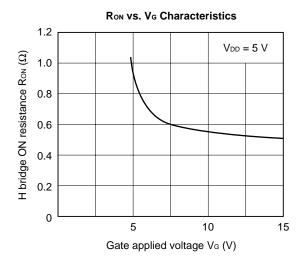
TYPICAL CHARACTERISTICS (TA = 25 °C)

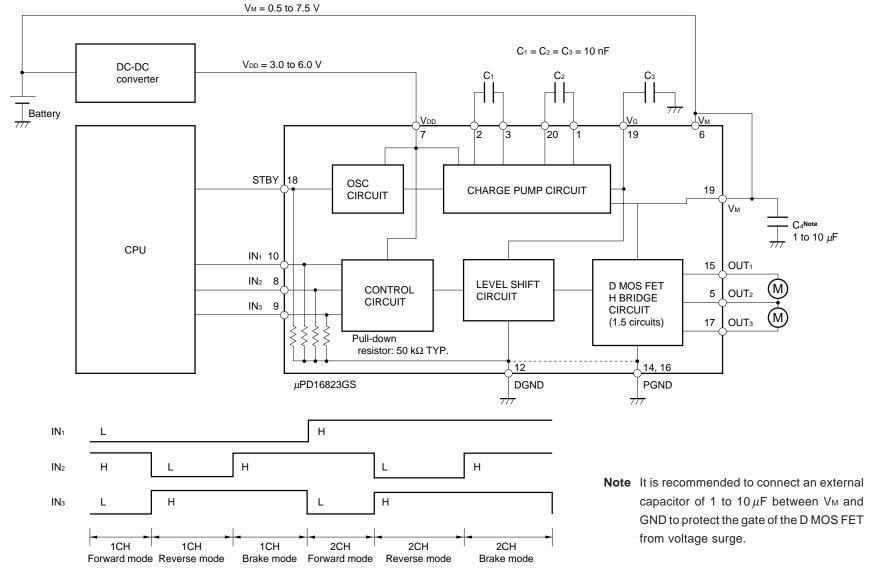








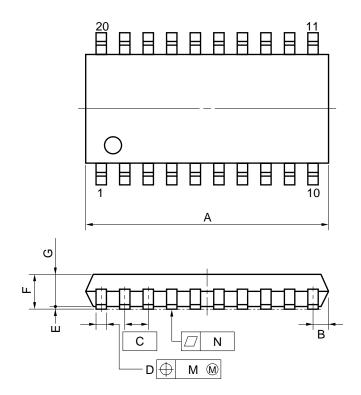




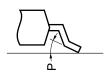


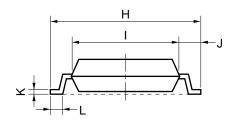
PACKAGE DIMENSION

20 PIN PLASTIC SOP (300 mil)



detail of lead end





NOTE

Each lead centerline is located within 0.12 mm (0.005 inch) of its true position (T.P.) at maximum material condition.

ITEM	MILLIMETERS	INCHES
Α	13.00 MAX.	0.512 MAX.
В	0.78 MAX.	0.031 MAX.
С	1.27 (T.P.)	0.050 (T.P.)
D	$0.40^{+0.10}_{-0.05}$	$0.016^{+0.004}_{-0.003}$
Е	0.1±0.1	0.004±0.004
F	1.8 MAX.	0.071 MAX.
G	1.55	0.061
Н	7.7±0.3	0.303±0.012
- 1	5.6	0.220
J	1.1	0.043
K	$0.20^{+0.10}_{-0.05}$	$0.008^{+0.004}_{-0.002}$
L	0.6±0.2	0.024+0.008
М	0.12	0.005
N	0.10	0.004
Р	3°+7°	3°+7°

P20GM-50-300B, C-4



RECOMMENDED SOLDERING CONDITIONS

It is recommended to solder this product under the conditions shown below.

For soldering methods and conditions other than those listed below, consult NEC.

For details of the recommended soldering conditions, refer to Information Document "Semiconductor Device Mounting Technology Manual" (C10535E).

Soldering Method	Soldering Condition	Symbol of Recommended Soldering
Infrared reflow	Package peak temperature: 235 °C, Time: 30 seconds MAX. (210 °C MIN.) Number of times: 2 MAX., Number of days: None ^{Note} , Flux: Rosin-based flux with little chlorine component (chlorine: 0.2 Wt% MAX.)	IR35-00-2
VPS	Package peak temperature: 215 °C, Time: 40 seconds MAX. (200 °C MIN.) Number of times: 2 MAX., Number of days: None ^{Note} , Flux: Rosin-based flux with little chlorine component (chlorine: 0.2 Wt% MAX.)	VP15-00-2
Wave soldering	Package peak temperature: 260 °C, Time: 10 seconds MAX., Preheating temperature: 120 °C MAX., Number of times: 1, Flux: Rosin-based flux with little chlorine component (chlorine: 0.2 Wt% MAX.)	WS60-00-1

Note The number of days during which the product can be stored at 25 °C 65% RH MAX. after the dry pack was opened.

Caution Do not use two or more soldering methods in combination.

REFERENCE DOCUMENTS

Document Name	Document No.
NEC Semiconductor Device Reliability/Quality Control System	C11745E
Guide to Quality Assurance for Semiconductor Devices	MEI-1202
Safe Operating Area of Power MOS FET	TEA-1037

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Standard: Computers, office equipment, communications equipment, test and measurement equipment, audio and visual equipment, home electronic appliances, machine tools, personal electronic equipment and industrial robots

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Anti-radioactive design is not implemented in this product.

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